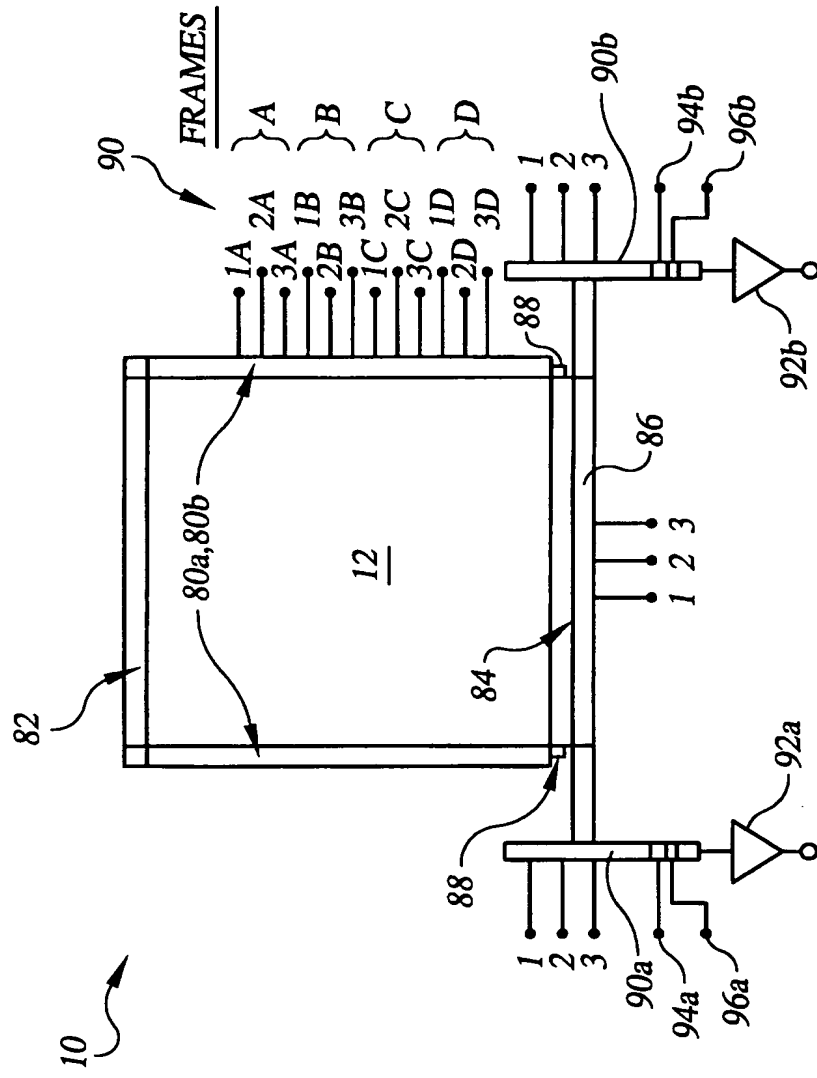




FIG. 1





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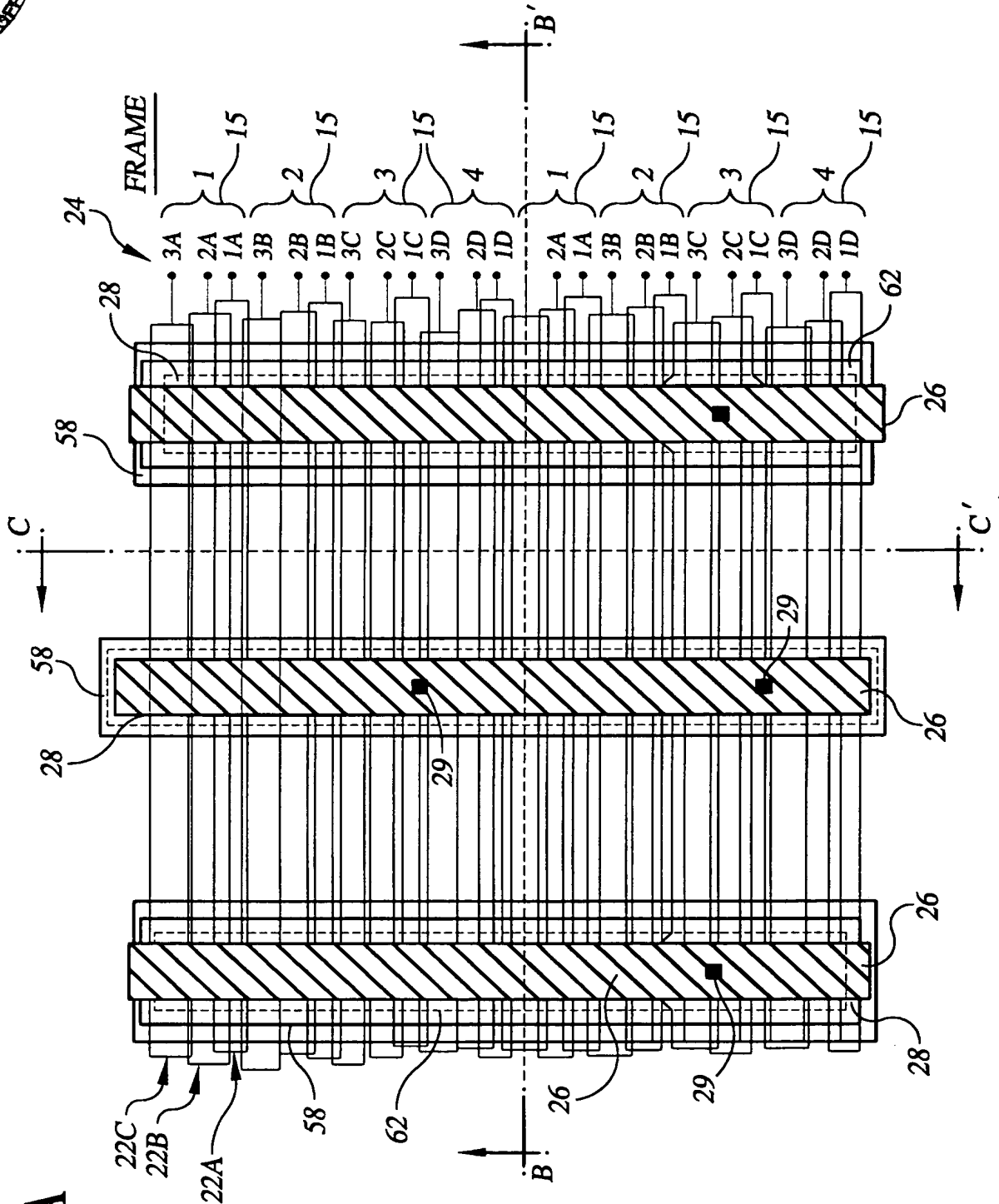
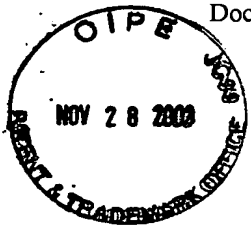
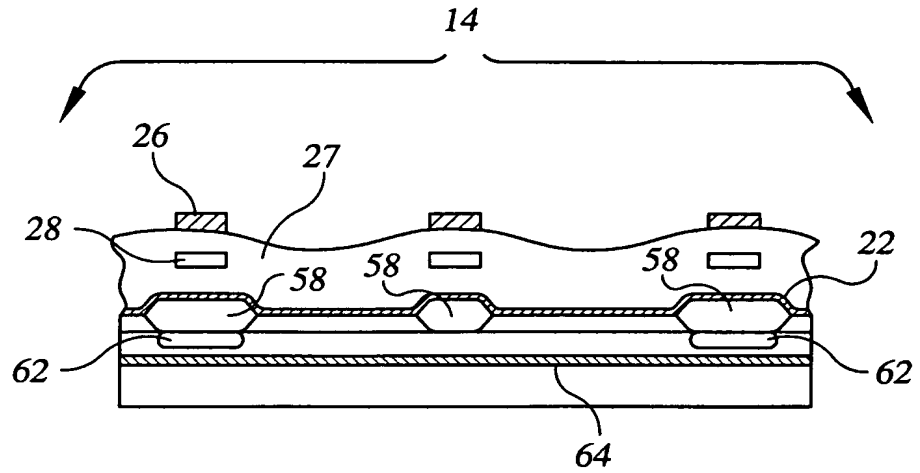


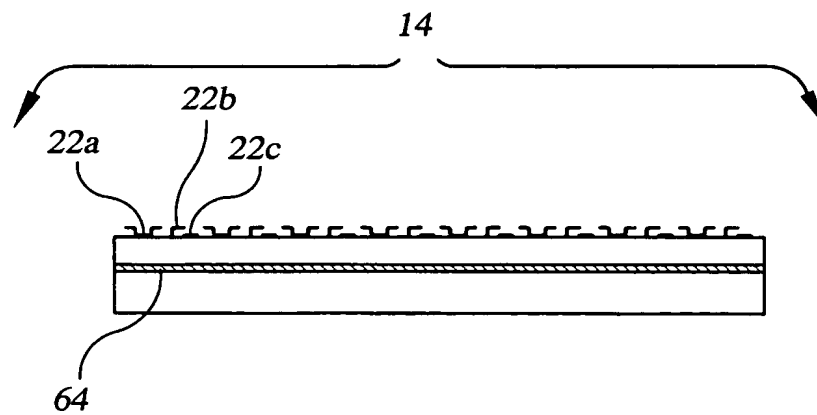
FIG. 2A

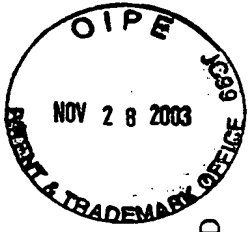


**FIG.2B**



**FIG.2C**





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FIG.3B

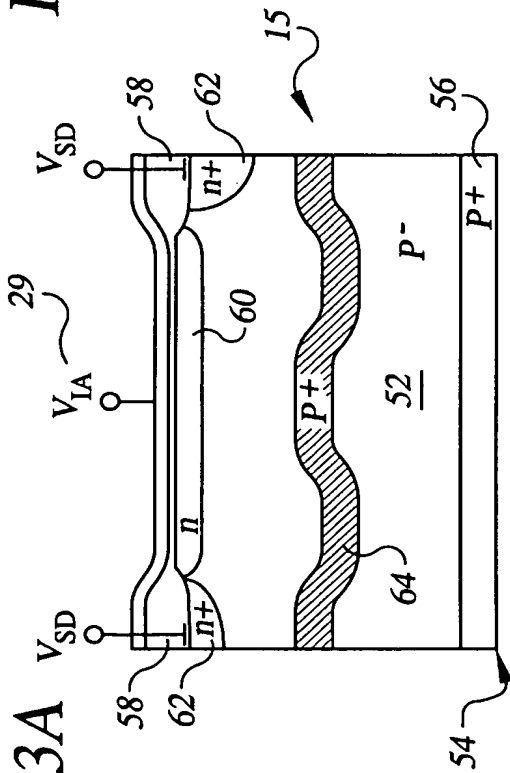


FIG.3A

FIG.3C

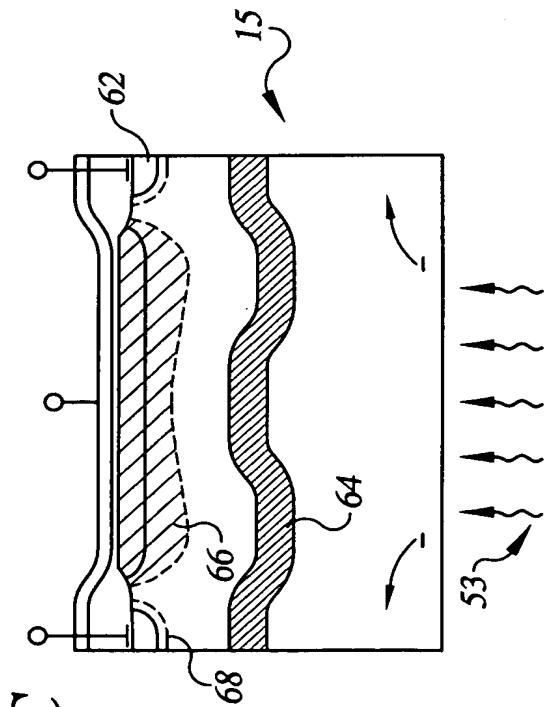


FIG.3D

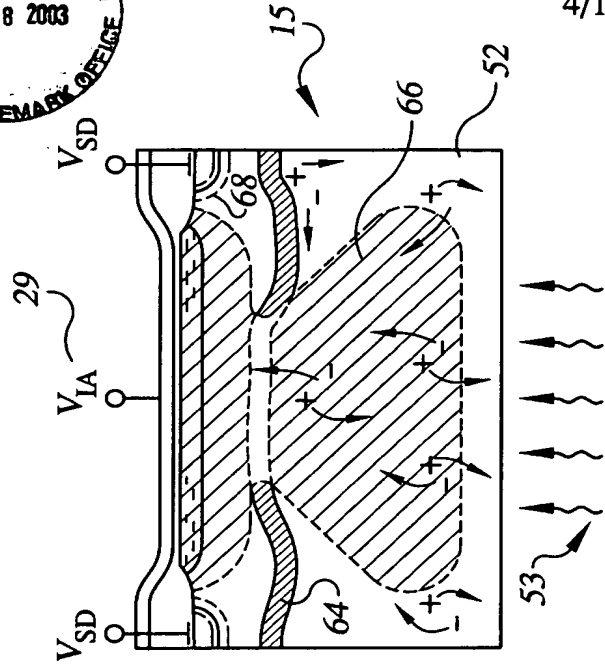
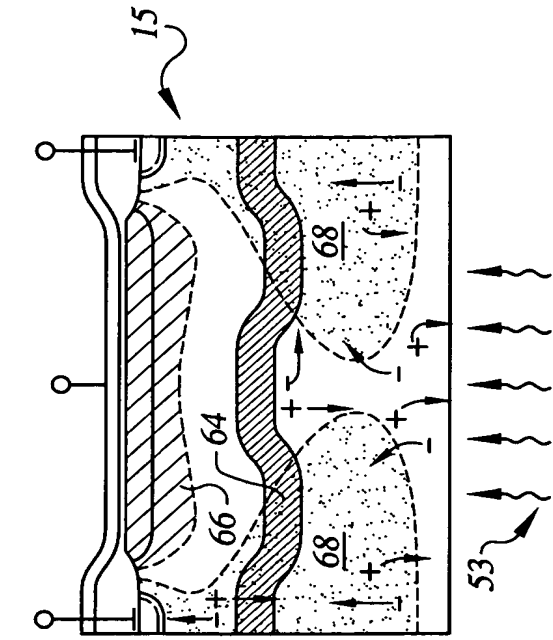




FIG.4A

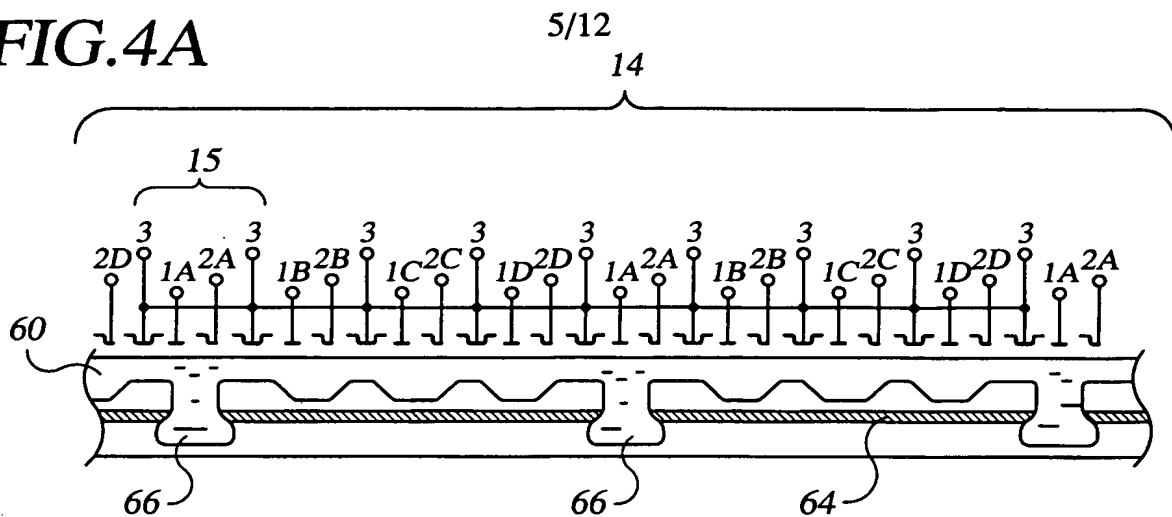


FIG.4B

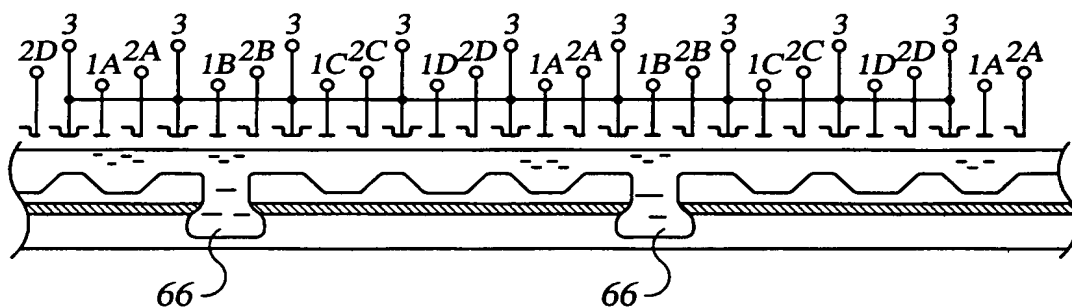


FIG.4C

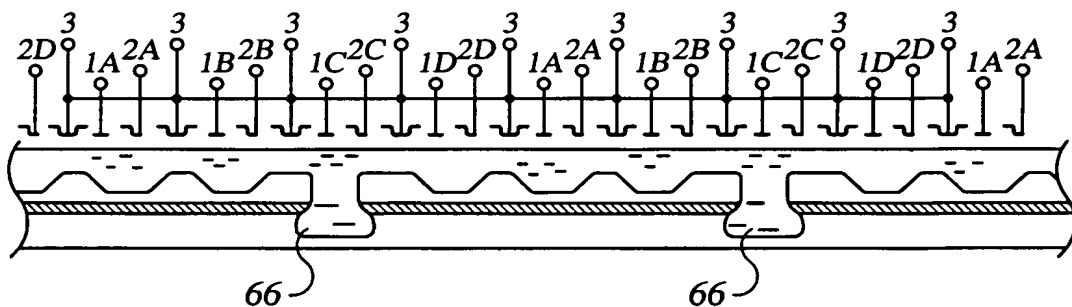
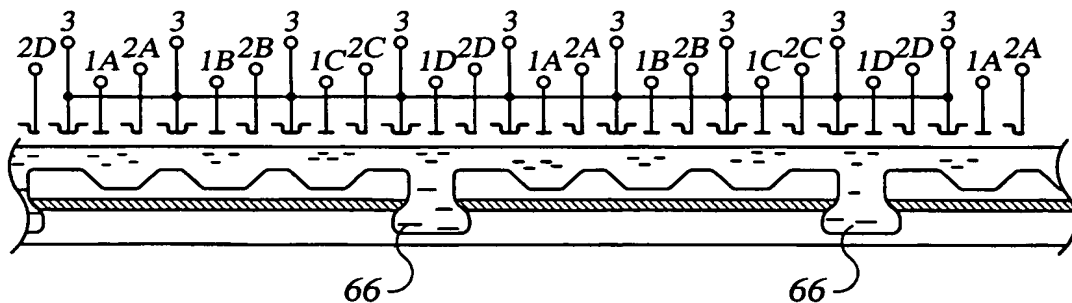


FIG.4D





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FIG. 5

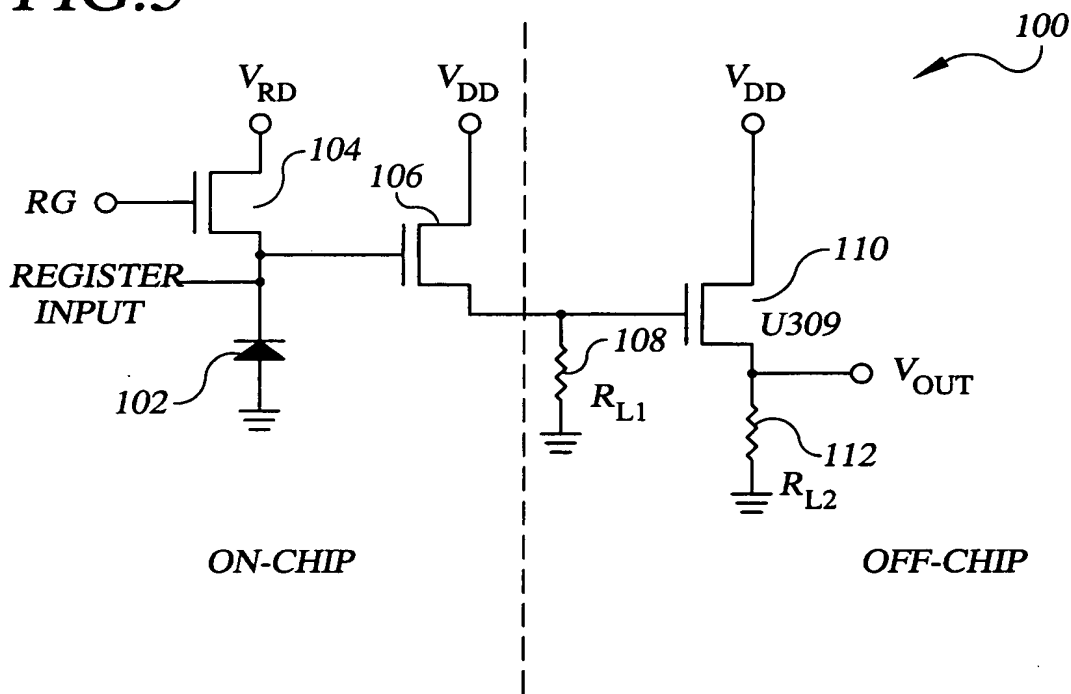
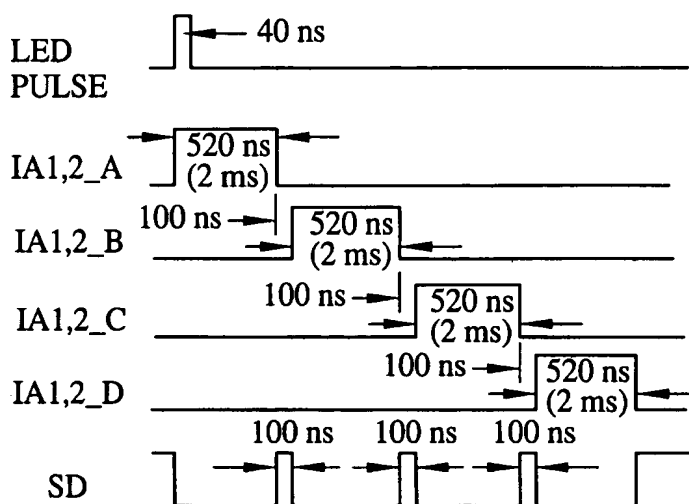


FIG. 8

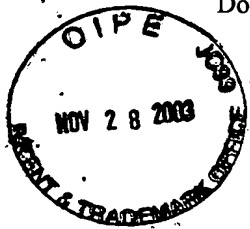




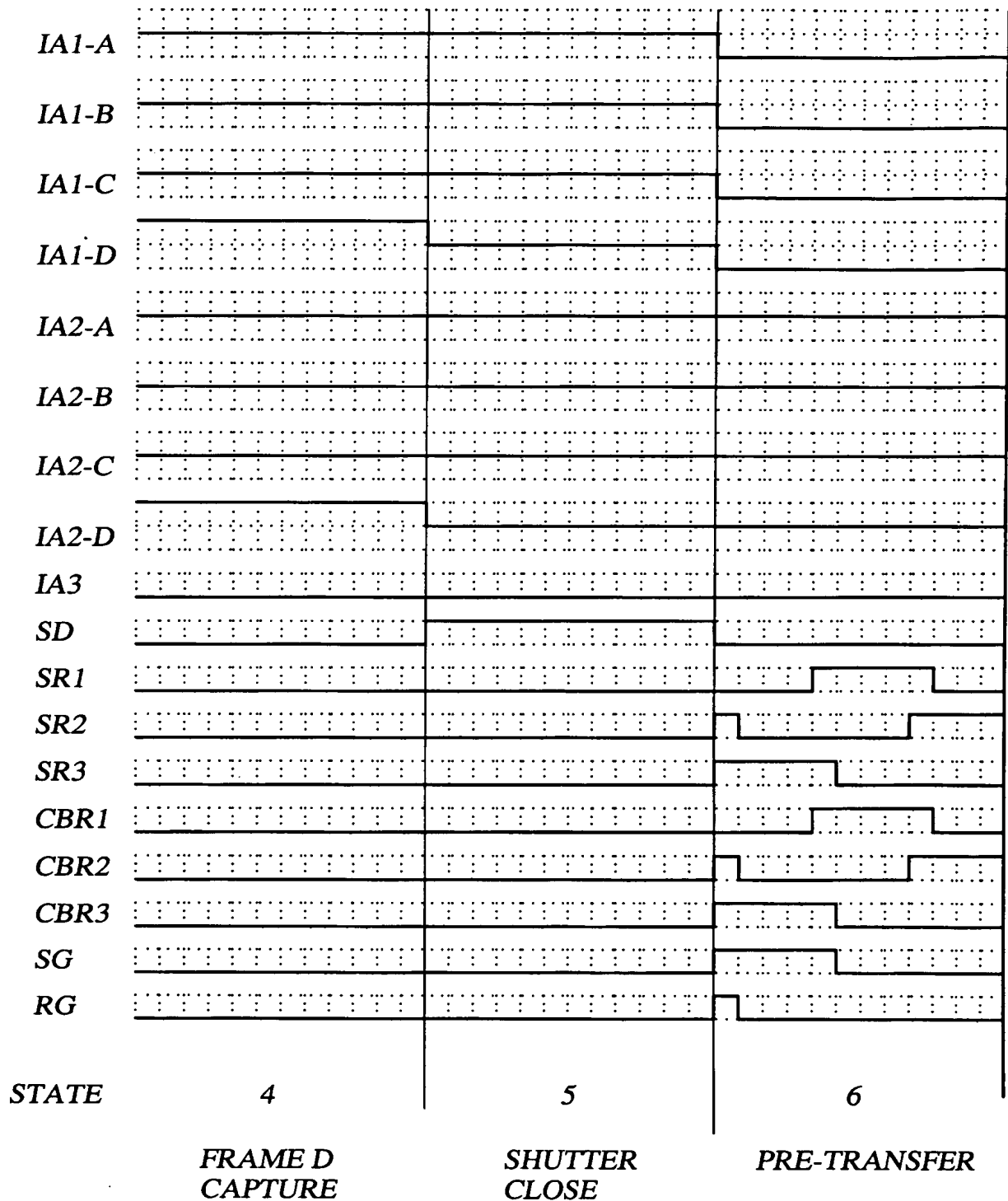
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**FIG. 6A**

IA1-A			
IA1-B			
IA1-C			
IA1-D			
IA2-A			
IA2-B			
IA2-C			
IA2-D			
IA3			
SD			
SR1			
SR2			
SR3			
CBR1			
CBR2			
CBR3			
SG			
RG			
STATE	1	2	3
	FRAME A CAPTURE	FRAME B CAPTURE	FRAME C CAPTURE



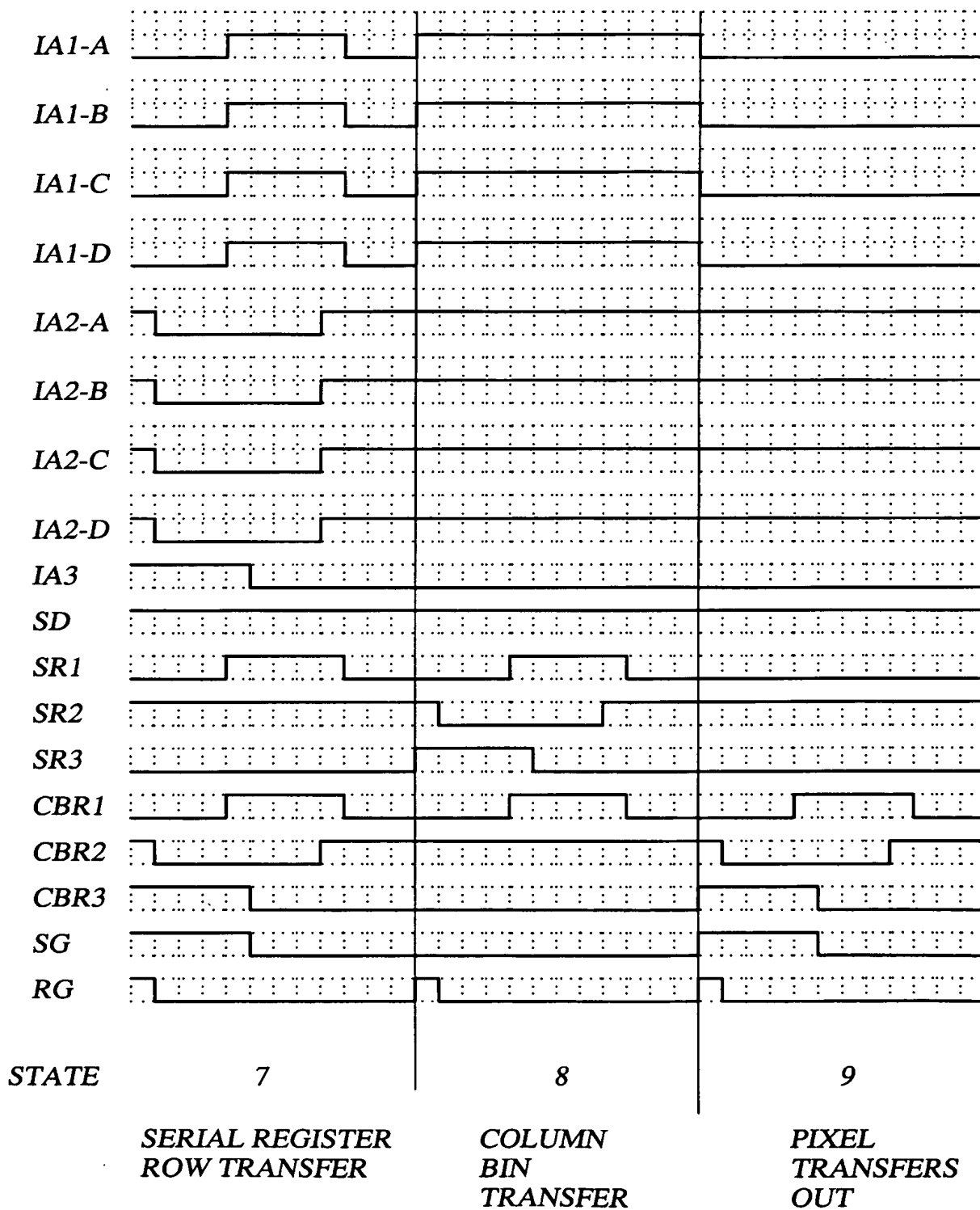
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**FIG. 6B**



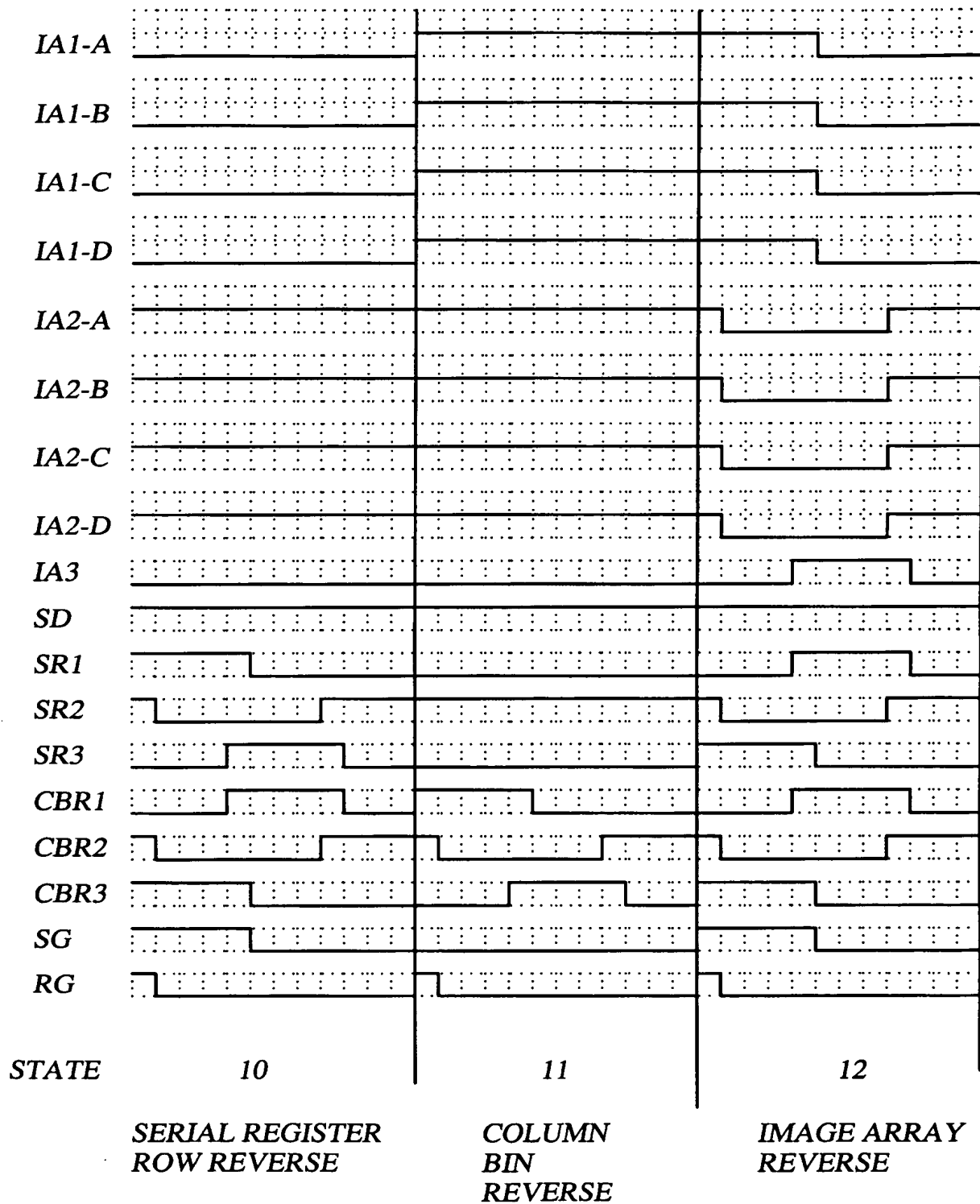


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**FIG.6C**



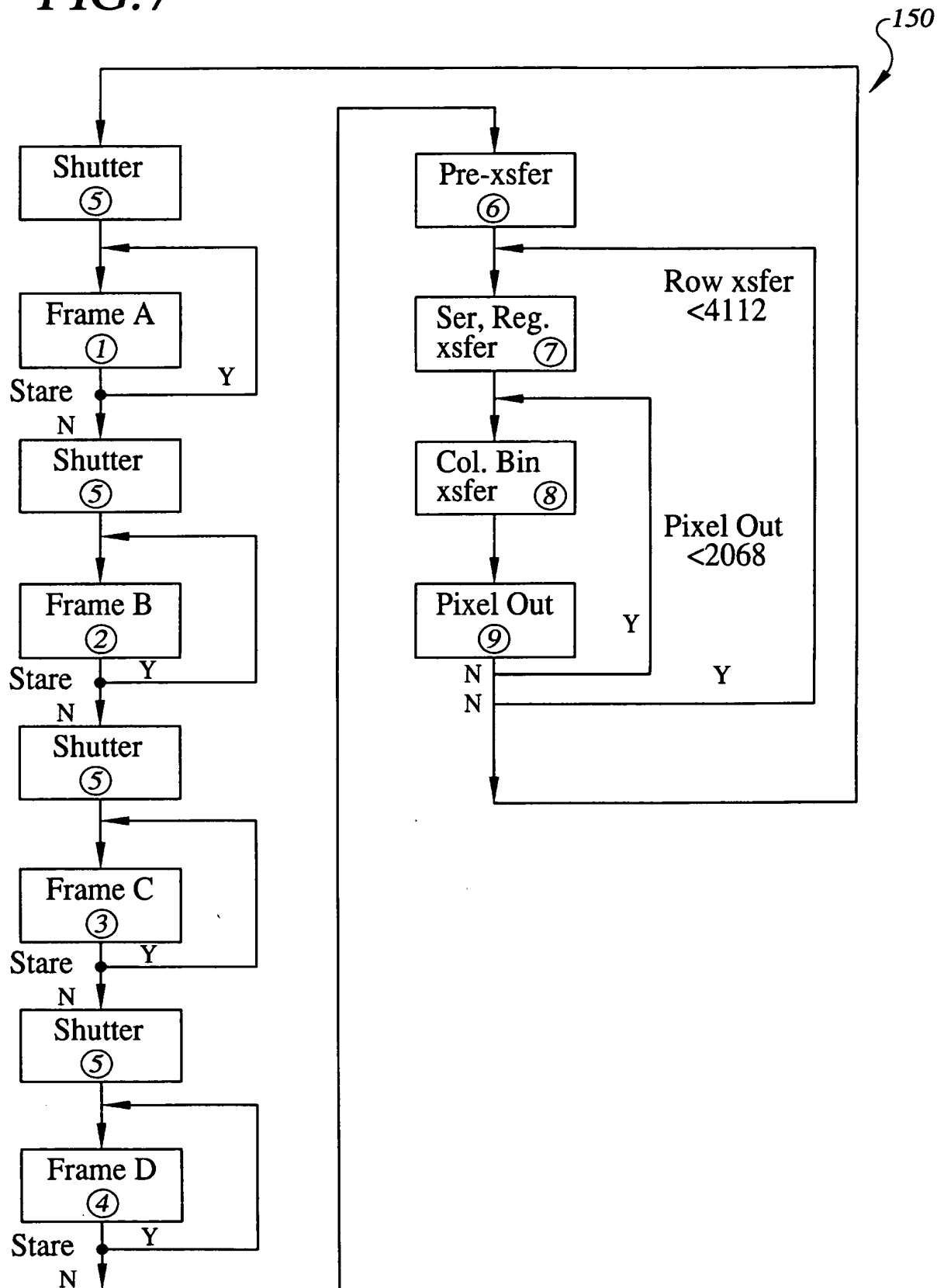
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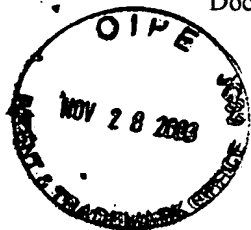
**FIG. 6D**



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FIG. 7





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FIG. 9

